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issues. This paper unveils the inner workings of these four SerDes architectures, examines their differences, and shows how each fits an important range of today's applications. Author(s) Biography Dave Lewis is a Technical Marketing Manager in National Semiconductor's PC & Networking Group, handling high-speed interface products.

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High Speed Serdes Devices and Applications

vi High Speed Serdes Devices and Applications introduces concepts related to interface standards, as well as design architectures for various protocol logic functions.

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ARCHITECTURE OF HIGH-SPEED SERDES main device in this operation. It explains function of SerDes and High-speed Serializer/Deserializer (HSS) devices are the different techniques for implementing it.

High speed serial link design (SERDES) Introduction

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To achieve high data transfer rates, Stratix® devices support True- LVDS TM differential I/O interfaces which have dedicated serializer/deserializer (SERDES) circuitry for each differential I/O pair.

5. High-Speed Differential I/O Interfaces in Stratix Devices

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